

IchigoJam R ピン配置

IchigoJam R	QFP48	QFN36	5V tolerant	function1	function2	function3	function4	function5	function6	function7
-		1		VBAT						
*		2		PC13	TAMPER-RTC					
*		3		PC14	OSC32IN					
*		4		PC15	OSC32OUT					
XTAL 8MHz		5	2	OSCIN	PDO					
XTAL 8MHz		6	3	OSCOUT	PD1					
RESET		7	4	NRST						
GND		8	5	VSSA						
VCC		9	6	VDDA						
BTN		10	7	PA0	WKUP	USART1_CTS	ADC01_IN0	TIMER1_CH0_ TIMER4_CH0		
IN1		11	8	PA1	USART1 RTS	ADC01_IN1	TIMER4_CH1	TIMER1_CH1		
OUT5		12	9	PA2	USART1 TX	TIMER4_CH2	ADC01_IN2	TIMER1_CH2		
OUT6		13	10	PA3	USART1_RX	TIMER4_CH3	ADC01_IN3	TIMER1_CH3		
IN2		14	11	PA4	SPI0 NSS	USART1 CK	ADC01_IN4	DAC_OUT0	SPI2 NSS	I2S2 WS
IN3		15	12	PA5	SPI0 SCK	ADC01_IN5	DAC_OUT1			
IN4		16	13	PA6	SPI0 MISO	ADC01_IN6	TIMER2_CH0	TIMERO_BRKIN		
OUT2		17	14	PA7	SPI0 MOSI	ADC01_IN7	TIMER2_CH1	TIMERO_CH0_ON		
OUT3		18	15	PB0	ADC01_IN8	TIMER2 CH2	TIMERO_CH1_ON			
OUT4		19	16	PB1	ADC01_IN9	TIMER2 CH3	TIMERO_CH2_ON			
EX1		20	17 5VT	PB2	BOOT1					
*		21	5VT	PB10	I2C1 SCL	USART2 TX	TIMER1 CH2			
*		22	5VT	PB11	I2C1 SDA	USART2 RX	TIMER1 CH3			
GND		23	18	VSSA						
VCC		24	19	VDDA						
*		25	5VT	PB12	SPI1 NSS	I2S1 WS	I2C1 SMBA	USART2 CK	TIMERO_BR1	CAN1_RX
*		26	5VT	PB13	SPI1 SCK	I2S1 CK	USART2 CTS	TIMERO_CH0_	CAN1 TX	
*		27	5VT	PB14	SPI1 MISO	USART2 RTS	TIMERO_CH1_ON			
*		28	5VT	PB15	SPI1 MOSI	TIMER0 CH2_	I2S1 SD			
OUT1		29	20 5VT	PA8	USART0 CK	TIMER0 CH0	CK_OUT0	USBFS SOF		
TXD		30	21 5VT	PA9	USART0 TX	TIMER0 CH1	USBFS_VBUS			
RXD		31	22 5VT	PA10	USART0 RX	TIMER0 CH2	USBFS_ID			
KBD2		32	23 5VT	PA11	USART0 CTS	CANO RX	TIMER0 CH3	USBFS_DM		
KBD1		33	24 5VT	PA12	USART0 RTS	CANO TX	TIMER0 ETI	USBFS_DP		
DC		34	25 5VT	JTMS	PA13					
GND		35	26	VSS						
VCC		36	27	VDD						
LED		37	28 5VT	JTCK	PA14					
SOUND		38	29 5VT	JTDI	SPI2 NSS	I2S2 WS	TIMER1 CH0_	PA15	SPI0 NSS	
SCK		39	30 5VT	JTDO	SPI2 SCK	I2S2 CK	PB3	TIMER1 CH1	SPI0 SCK	
VIDEO1/CIPO		40	31 5VT	NJTRST	SPI2 MISO	TIMER2 CH0	PB4	SPI0 SCK		
VIDEO2/COPI		41	32	PB5	I2C0 SMBA	SPI2 MOSI	I2S2 SD	TIMER2 CH1	SPI0 MOSI	CAN1 TX
SCL		42	33 5VT	PB6	I2C0 SCL	TIMER3 CH0	USART0 TX	CAN1 TX		
SDA		43	34 5VT	PB7	I2C0 SDA	TIMER3 CH1	USART0 RX			
ISP		44	35	BOOT0						
*		45	5VT	PB8	TIMER3 CH2	I2C0 SCL	CANO RX			
*		46	5VT	PB9	TIMER3 CH3	I2C0 SDA	CANO TX			
GND		47	36	VSS						
VCC		48	1	VDD						

CN6 外付け液晶 ILI9341などとの接続用

5V

5V

CIPO VIDEO1を配線 (CIPO controller input peripheral output、元MISO)

COPI VIDEO2を配線 (COPI controller output peripheral input、元MOSI)

SCK

DC

RESET (起動後、ディスプレイ制御用に一時的に GPIOにして一度 GNDにする)

VCC

GND

CN5

5V
SDA
SCL
VCC
GND

CN4

VIDEO1 VIDEO1 - 470Ω -+ ビデオ端子 - GND
VIDEO2 VIDEO2 - 100Ω -+
IN1
IN2
IN3
IN4
VCC
GND
OUT1
OUT2
OUT3
OUT4
BTN
LED LED - 680Ω - (赤色LED) - GND

CN3

KBD1
NC
KBD2
SOUND SOUND - (サウンダー) - GND
ISP ISP - 10kΩ-GND
RESET
GND
VCC
SCL 元XTAL (ブルアップは内蔵しない)
SDA 元XTAL (ブルアップは内蔵しない)
OUT5
OUT6
TXD
RXD

XTAL

OSCIN OSCIN - XTAL -- 15pF - GND
OSCOUT OSCOUT --+---- 15pF - GND